

(PW)

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Teck Kheng Lee

Serial No.: 10/782,270

Filed: February 18, 2004

**For: INTERPOSER SUBSTRATE AND
WAFER SCALE INTERPOSER
SUBSTRATE MEMBER FOR USE WITH
FLIP-CHIP CONFIGURED
SEMICONDUCTOR DICE**

Confirmation No.: 4215

Examiner: J. Clark

Group Art Unit: 2815

**Attorney Docket No.: 2269-4973.1US
(00-0593.01/US)**

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

May 9, 2005
Date

Signature

Joseph A. Walkowski
Name (Type/Print)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO/SB/08 be considered by the Examiner and made of record. Pursuant to M.P.E.P. 609 III A(2), a copy of this U.S. patent is not being provided.

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicant herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

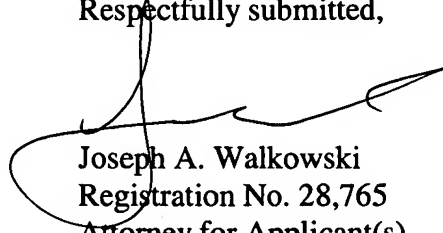
U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 6,239,496	05/29/2001	Asada

Applicant offers to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is believed to be filed before the mailing date of the first Office Action on the merits subsequent to the filing of an RCE in the above-identified application.

Respectfully submitted,

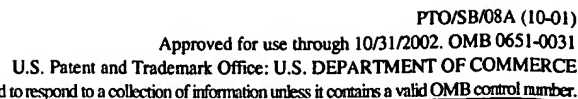


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Enclosures: Form PTO/SB/08

Document in ProLaw



Sheet	1	of	1
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Application Number	10/782,270
Filing Date	February 18, 2004
First Named Inventor	Teck Kheng Lee
Group Art Unit	2815
Examiner Name	J. Clark
Attorney Docket Number	2269-4973.1US (00-0593.01/US)

[illegible][illegible]Date
Considered

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**